

PSA TEST RESULTS

June 08 2010 3:55 AM

Port Count..... 8
 Loop Count..... 1
 PSE Tested: Sample 8-Port Type-2 PSE



802.3at Conformance Report

Test Mode: 30 Watt LLDP
 Sifos Interop Index*: 99%
 Error Log: None

version 4.0.0
 report version 3.5c

Chassis ID: 192.168.221.141

TestLoop: 1

Test	PSA-3000 Ports						UNITS	Min	Max	Average	Low Limit	P/F	High Limit	P/F		
	1-1	1-2	2-1	2-2	3-1	3-2									4-1	4-2
Test: det_v																
Open_Circuit_Det_Voc=	10.43	10.4	10.4	10.38	10.32	10.28	10.4	10.38	volts	10.28	10.43	10.37375	2.8	Pass	30	Pass
Peak_Det_Vvaid=	8	7.97	7.97	7.96	8	7.96	7.96	7.95	volts	7.95	8	7.97125	3.8	Pass	10	Pass
Min_Det_Vvaid=	4.01	4	4	4	4.02	3.98	4.02	4	volts	3.98	4.02	4.00375	2.8	Pass	9	Pass
Det_Volt_Step_dVtest=	3.47	3.45	3.44	3.39	3.45	3.41	3.43	3.4	volts	3.39	3.47	3.43	1	Pass	7.2	Pass
Detection_Slew=	0	0	0	0	0	0	0	0	V/usec	0	0	0	0	Pass	0.1	Pass
Good_Sig_Det_Pulse=	3	3	3	3	3	3	3	3	edges	3	3	3	1	Pass	9	Pass
Backoff_Voltage=	0.5	0.5	0.5	0.5	0.5	0.5	0.5	0.5	volts	0.5	0.5	0.5	0	Pass	9	Pass
Non_802_Step_V=	0	0	0	0	0	0	0	0	volts	0	0	0	0	Pass	0.1	Pass
High_Sig_MaxV=	10.1	10.03	10.04	10.08	10.09	10.1	10.07	10.11	volts	10.03	10.11	10.0775	3.8	Pass	11	Pass
Non_802_Discr_?=	0	0	0	0	0	0	0	0	****	0	0	0	0	Pass	0	Pass
Detect_Strategy=	0	0	0	0	0	0	0	0	****	0	0	0	0	Pass	2	Pass
Test: det_i																
Init_Current_Isc=	0.19	0.19	0.19	0.19	0.19	0.18	0.18	0.19	mA	0.18	0.19	0.1875	0	Pass	5	Pass
Det_Current_Isc=	0.18	0.18	0.18	0.18	0.17	0.17	0.17	0.17	mA	0.17	0.18	0.175	0	Pass	5	Pass
Test: det_range																
Rgood_Max=	29	29	29	29	29	29	29	29	Kohm	29	29	29	26	Pass	32	Pass
Rgood_Min=	18	18	18	18	18	18	17	17	Kohm	17	18	17.75	16	Pass	19	Pass
Rmid_det=	29	29	29	29	29	29	29	29	Kohm	29	29	29	26	Pass	33	Pass
Rgood_Max=	0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1	uF	0.1	0.1	0.1	0	Pass	10	Pass
Rbad_Cbad_Stat=	0	0	0	0	0	0	0	0	****	0	0	0	0	Pass	0	Pass
Test: det_time																
Backoff_Time_TdBo=	141	148	141	141	141	148	148	148	msec	141	148	144.5	-1	Pass	16000	Pass
Eff_Backoff_TdBo_eff=	1400	148	141	141	141	148	148	1400	msec	141	1400	458.375	-1	Pass	16000	Pass
Backoff_Type=	0	0	0	0	0	0	0	0	****	0	0	0	0	Pass	1	Pass
Detection_Time_Tdet=	301	309	305	301	305	305	309	309	msec	301	309	305.5	5	Pass	500	Pass
Total_Det_Time=	309	309	309	309	309	316	309	309	msec	309	316	309.875	5	Pass	1000	Pass
Test: det_rsource																
Output_Impedance_Zout=	359.3	326.5	342.2	342.2	326.5	286.2	342.2	378.1	KOhm	286.2	378.1	337.9	45	Pass	2000	Pass
Test: class_v																
Class_Voltage_Vclass=	17.3	17.6	17.6	16.6	17.7	17.7	17.6	17.5	volts	16.6	17.7	17.45	15.5	Pass	20.5	Pass
Vclass_Min=	17.6	17.7	17.5	17.4	17.5	17.5	17.4	17.3	volts	17.3	17.7	17.4875	15.5	Pass	20.5	Pass
Test: class_time																
Event_Count=	1	1	1	1	1	1	1	1	****	1	1	1	1	Pass	2	Pass
Class_Time_TpdC=	15.6	13.6	13.6	13.6	13.6	13.6	13.6	13.6	msec	13.6	15.6	13.85	6	Pass	75	Pass
Test: class_err																
Class_lim=	65	65	65	65	65	65	65	65	mA	65	65	65	51	Pass	100	Pass
Vport_Cl_lim=	14.7	14.9	14.9	15.1	15.1	15.1	14.9	15.1	V	14.7	15.1	14.975	0	Pass	20.5	Pass
Vport_Cl_err_1=	17.1	16.9	17	17	17.1	17.1	16.9	16.9	V	16.9	17.1	17	0	Pass	20.5	Pass
Test: class_lldp																
PSE_Source_Priority=	0	0	0	0	0	0	0	0	*	0	0	0	0	Pass	0	Pass
PSE_MDI_Pwr_Sup=	0	0	0	0	0	0	0	0	*	0	0	0	0	Pass	0	Pass
PSE_LLDP_Time_2=	2.8	2.7	2.8	2.4	2.7	16.6	2.6	16.7	sec	2.4	16.7	6.1625	0	Pass	10	Fail
PSE_LLDP_Type_2=	2	2	2	2	2	2	2	2	*	2	2	2	2	Pass	2	Pass
PSE_Echo_Time_2=	4	1.7	0.2	4	2.1	5.9	1.7	4.7	sec	0.2	5.9	3.0375	0	Pass	10	Pass
PSE_Alloc_Pwr_2=	20.3	20.3	20.3	20.3	20.3	20.3	20.3	20.3	Watts	20.3	20.3	20.3	20.3	Pass	25.5	Pass
PSE_Alloc_Time_2=	4	1.7	0.2	4	2.1	5.9	1.7	4.7	sec	0.2	5.9	3.0375	0	Pass	30	Pass
PD_Power_Adjust_2=	25.5	25.5	25.5	25.5	25.5	25.5	25.5	25.5	Watts	25.5	25.5	25.5	25.5	Pass	25.5	Pass
PSE_Adjust_Time_2=	2.1	4	2.1	2.1	3.6	3.2	4	3.2	sec	2.1	4	3.0375	0	Pass	10	Pass
Test: pwrup_time																
Pwr-On_Rise_Time_Trise=	16	35	16	44	37	16	15	41	uSec	15	44	27.5	15	Pass	50000	Pass
Power-On_Time_Tpon=	7.8	62.5	66.4	128.9	15.6	15.6	15.6	46.9	msec	7.8	128.9	44.9125	0	Pass	400	Pass
Test: pwrup_inrush																
Init_Inrush=	430	428.63	428.38	427.75	428.25	432.25	429	428.63	mA	427.75	432.25	429.1125	400	Pass	512	Pass
Max_Inrush_c4=	429	428	427.13	426.75	427.25	431	428.88	427.5	mA	426.75	431	428.18875	400	Pass	450	Pass
Min_Inrush=	427	426.13	425.13	425.5	425.88	429.13	426.63	425.63	mA	425.13	429.13	426.37875	400	Pass	450	Pass
Tinrush=	59.2	59.2	59.2	59.2	59.6	59.6	59.6	59.6	msec	59.2	59.6	59.4	50	Pass	75	Pass
Inrush_45m=	54.4	54.1	54.2	54.2	54.2	54.3	54.1	54.3	Volts	54.1	54.4	54.225	50	Pass	57	Pass
Inrush_Voltage=	35.3	35.1	35.2	35.2	35.3	35.3	35.5	35.4	Volts	35.1	35.5	35.3125	30	Pass	57	Pass
Max_Init_Inrush=	714	713	713.8	713.3	713	714.5	713.3	714.3	mA	713	714.5	713.65	0	Pass	2000	Pass
Inrush_Strategy=	0	0	0	0	0	0	0	0	****	0	0	0	0	Pass	0	Pass
Test: pwrn_v																
Vport_min_2=	53.6	53.4	53.4	53.4	53.4	53.6	53.3	53.5	V	53.3	53.6	53.45	50	Pass	57	Pass
Vport_max_2=	54.6	54.3	54.3	54.3	54.3	54.5	54.3	54.4	V	54.3	54.6	54.375	50	Pass	57	Pass
Vport_ripple_2=	6	6	6	6	7	5	6	6	mVpp	5	7	6	0	Pass	500	Pass
Vport_noise_2=	10	10	9	8	7	9	9	9	mVpp	7	10	8.875	0	Pass	200	Pass
Vtrans_min_2=	53	52.8	52.7	52.8	52.8	52.9	52.7	52.8	V	52.7	53	52.8125	50	Pass	57	Pass
Vtrans_max_2=	54.8	54.4	54.5	54.5	54.5	54.7	54.4	54.6	V	54.4	54.8	54.55	50	Pass	57	Pass
Test: pwrn_pwrCap																
Pcon_c4=	31.4	31.5	31.3	31.4	31.6	31.3	31.4	31.4	watts	31.3	31.6	31.4125	30	Pass	38.9	Pass
Icon_c4=	587	587	584	584	587	584	584	584	mA	584	587	585.125	526.3	Pass	683	Pass
Type-2_Enable=	0	0	0	0	0	0	0	0	****	0	0	0	0	Pass	0	Pass
Test: pwrn_maxi																
Ilim_Peak=	362.5	363	365	364	360.8	367.5	364.5	369	mA	360.8	369	364.5375	0	Pass	1750	Pass
Ilim_Min_2=	686.8	685.8	686.5	685.5	686	687	686	687	mA	686.5	687	686.325	683	Pass	1750	Pass
Tlim_2=	61.3	60.5	60.9	60.9	62.5	62.1	61.7	62.1	msec	60.5	62.5	61.5	10	Pass	75	Pass
Vlim_2=	53.2	53	52.9	53	53	53.2	52.9	53	V	52.9	53.2	53.025	50	Pass	57	Pass
Ilim_Max_2=	861.3	860	860.8	860	860.5	861.5	860.3	861.5	mA	860	861.5	860.7375	0	Pass	1750	Pass
Ilim_Low_V_Tol_2=	59.8	59.4	60.2	60.2	59	60.5	60.5	59.4	msec	59	60.5	59.875	10	Pass	9999	Pass
Ktran_lo=	106.4	106	105.9	106	106	106.3	105.8	106.1	%	105.8	106.4	106.0625	92.4	Pass	115	Pass
Test: pwrn_overld																
Ipeak_2=	125	125	125	125	125	125	125	125	%	125	125	125	0	Pass	125	Pass
Vport_Ipeak_2=	53.3	53	53	53	53.1	53.2	53	53.1	V	53	53.3	53.0875	50	Pass	57	Pass
Vport_5%DC_2=	53.4	53.1	53	53.1	53.1	53.3	53	53.1	V	53	53.4	53.1375	50	Pass	57	Pass
Test: mps_dc_valid																
Min_Valid_Time_Tmps=	10	10	10	10	10	10	10	10	msec	10	10	10	1	Pass	60	Pass
Duty_Cycle_tol=	1	1	1	1	1	1	1	1	****	1	1	1	1	Pass	1	Pass
Test: mps_dc_pwrn																
Min_Valid_I_hold=	8	8	8	8	9	9	9	9	mA	8	9	8.5	5	Pass	10</	